

Application Number 09/653,701
Amendment dated November 21, 2004
Reply to Office action of May 21, 2004

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A video editing apparatus for performing video editing in realtime of at least two video streams derived from a codec able to retrieve two video sources from a data storage device and output two uncompressed video data streams, the apparatus comprising:

a video data storage device;
a graphics processor having at least two video inputs and a video output;
a video decoder having a video signal input and an uncompressed digital video output;
a video encoder having an uncompressed video data stream input and a display signal output;
a first video bus transferring data between said video output of said graphics processor and said video encoder when said apparatus is operating in a realtime video editing mode;
a second video bus transferring data between said codec and said at least two video inputs of said graphics processor when said apparatus is operating in a realtime video editing mode;
~~| said video decoder using one of said first and said second video buses to transfer data to said video encoder in a non-editing playback mode, and said video decoder using one of said first and said second video buses to transfer data to said codec in a video capture mode.~~

Claim 2 (canceled)

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Claim 3 (original): The apparatus as claimed in claim 2, wherein data is transferred from said video output of said graphics processor to said codec for compression and storage in said video data storage device.

Claim 4 (original): The apparatus as claimed in claim 3, wherein said graphics processor has an input buffer for storing a sequence of fields of said at least two video inputs and an output buffer for storing a sequence of fields of said graphics processor video output.

Claim 5 (original): The apparatus as claimed in claim 4, wherein said input buffer also stores input graphic image fields.

Claim 6 (original): The apparatus as claimed in claim 1, wherein said graphics processor has an input buffer for storing a sequence of fields of said at least two video inputs and an output buffer for storing a sequence of fields of said graphics processor video output.

Claim 7 (original): The apparatus as claimed in claim 6, wherein said input buffer also stores input graphic image fields.

Claim 8 (original): The apparatus as claimed in claim 1, wherein said second video bus is a single bus communicating data multiplexed on said bus to all of said at least two video input.

Claim 9 (original): The apparatus as claimed in claim 7, further comprising an input for compressed digital video input from external device, and a decompression device, one of said at least two video inputs comprising decompressed data from said compressed digital video input.

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Claim 10 (original): The apparatus as claimed in claim 9, wherein said compressed digital video input comprises one of an IEEE 1394 interface and an SDTI interface.